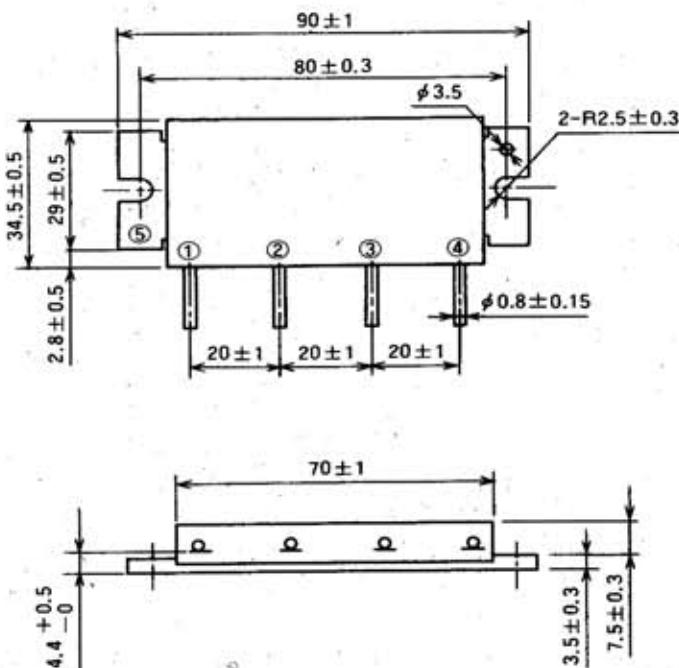
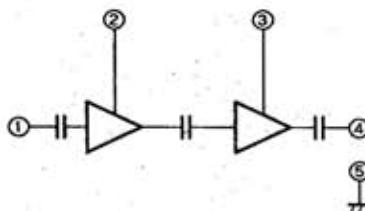


**OUTLINE DRAWING**

Dimensions in mm

**BLOCK DIAGRAM**

## PIN :

- ① Pin : RF INPUT
- ② VCC1 : 1st. DC SUPPLY
- ③ VCC2 : 2nd. DC SUPPLY
- ④ Po : RF OUTPUT
- ⑤ GND : FIN

H17

**ABSOLUTE MAXIMUM RATINGS** ( $T_c = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
Vcc	Supply voltage		17	V
Icc	Total current		25	A
Pin(max)	Input power	$Z_g = Z_L = 50 \Omega$	18	W
Po(max)	Output power	$Z_g = Z_L = 50 \Omega$	80	W
Tc(OP)	Operation case temperature		-30 to 110	°C
Tstg	Storage temperature		-40 to 110	°C

Note. Above parameters are guaranteed independently.

**ELECTRICAL CHARACTERISTICS** ( $T_c = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test conditions	Limits		Unit
			Min	Max	
f	Frequency range	$P_{in} = 10\text{W}$ $V_{cc} = 12.5\text{V}$ $Z_g = Z_L = 50 \Omega$	490	512	MHz
Po	Output power		50		W
$\eta\tau$	Total efficiency		40		%
2fo	2nd. harmonic			-30	dBc
3fo	3rd. harmonic			-35	dBc
$\rho_{in}$	Input VSWR			2.8	-
-	Load VSWR tolerance	$V_{cc} = 15.2\text{V}$ $P_{in} = 50\text{W}$ ( $P_{in}$ : controlled) Load VSWR=8.8:1 (All phase), 2sec. $Z_g = 50 \Omega$	No degradation or destroy		-

Note. Above parameters, ratings, limits and conditions are subject to change.