**TOSHIBA** 

#### TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# 2 S C 2 2 9 0

2~30MHz SSB LINEAR POWER AMPLIFIER APPLICATIONS. (LOW SUPPLY VOLTAGE USE)

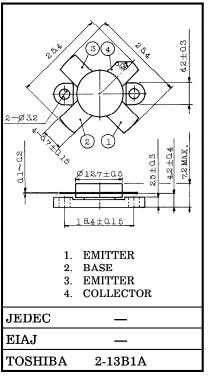
• Specified 12.5V, 28MHz Characteristics

 $\begin{array}{lll} \bullet & \text{Output Power} & : Po=60 \text{WpEp} \\ \bullet & \text{Power Gain} & : G_p=11.8 \text{dB (Min.)} \\ \bullet & \text{Collector Efficiency} & : \gamma_C=35\% \text{ (Min.)} \\ \bullet & \text{Intermodulation Distortion} & : IMD=-30 \text{dB (Max.)} \end{array}$ 

### MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Collector-Base Voltage	$v_{CBO}$	45	V	
Collector-Emitter Voltage	V <sub>CES</sub>	45	V	
Collector-Emitter Voltage	$v_{CEO}$	18	V	
Emitter-Base Voltage	$V_{ m EBO}$	4	V	
Collector Current	$I_{\mathbf{C}}$	20	A	
Collector Power Dissipation (Tc=25°C)	$P_{\mathbf{C}}$	175	w	
Junction Temperature	$T_{j}$	175	°C	
Storage Temperature Range	$T_{ m stg}$	-65~175	°C	

Unit in mm



Weight: 5.2g

## ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	$I_{C}=100mA, I_{B}=0$	18	_	_	V
Collector-Emitter Breakdown Voltage	V <sub>(BR)CES</sub>	$I_{C} = 100 \text{mA}, V_{EB} = 0$	45	_	_	V
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	$I_E=1mA, I_C=0$	4	_	_	V
DC Current Gain	hFE	$V_{CE}=5V, I_{C}=10A *$	10	_	150	_
Collector Output Capacitance	$C_{ob}$	$V_{CB} = 12.5V, I_{E} = 0$ f=1MHz	_	_	500	pF
Power Gain	$G_{\mathrm{p}}$	$V_{CC}$ =12.5V, f=28MHz 2-tone, $\Delta$ f=1kHz $I_{idle}$ =50mA Po=60WPEP (Fig.)	11.8	13.8	_	dB
Input Power	Pi			2.5	4	$W_{PEP}$
Collector Efficiency	$\eta_{\mathbf{C}}$		35		_	%
Intermodulation Distortion	IMD		_	_	-30	dB
Series Equivalent Input Impedance	Z <sub>in</sub>	$V_{CC}$ =12.5V, f=28MHz $\Delta f$ =1kHz, Po=60W <sub>PEP</sub>		$1.02 \\ -j0.17$		Ω
Series Equivalent Output Impedance	Z <sub>out</sub>			0.86 - j0.21	_	Ω

<sup>\*</sup> Pulse Test : Pulse Width≤100µs, Duty Cycle≤3%

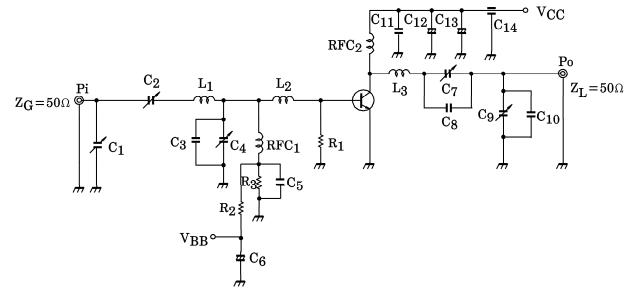
#### CAUTION

Beryllia Ceramics is used in this product. The dust or vapor can be dangerous to humans. Do not break, cut, crush or dissolve chemically. Dispose of this product properly according to law. Do not intermingle with normal industrial or domestic waste.

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TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

Fig. Pi TEST CIRCUIT

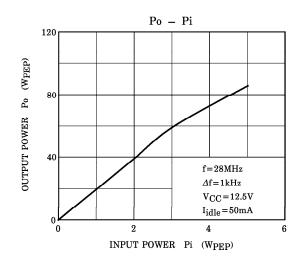


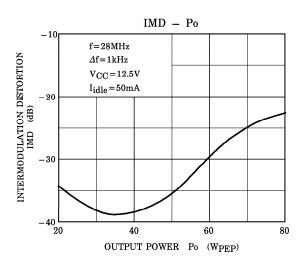
: φ0.8 ENAMEL COATED COPPER WIRE, 9ID, 6T  $L_1$ :  $\phi$ 1 SILVER PLATED COPPER WIRE, 9ID, 2T  $L_2$ :  $\phi$ 1.5 ENAMEL COATED COPPER WIRE, 9ID, 5T RFC<sub>1</sub>:  $\phi$ 0.8 ENAMEL COATED COPPER WIRE, 9ID, 20T RFC<sub>2</sub>:  $\phi$ 1.5 ENAMEL COATED COPPER WIRE, 12ID, 15T

C<sub>9</sub> C<sub>10</sub> : 10~200pF :  $5.6\Omega (1/2W)$ : 600pF  $R_2$ :  $5\Omega$  (5W) :  $0.4\mu$ F  $R_3$ :  $1.5\Omega (10W)$ 

 $C_{12}, C_{13} : 22 \mu F 35 WV$ : 1000pF

(FEED THROUGH)





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