Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK3476

VHF- and UHF-band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

- Output power: Po = 7.0 W (min)
- Gain: GP = 11.4dB (min)
- Drain efficiency: $\eta_D = 60\%$ (min)

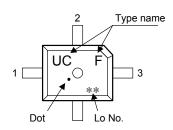
Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	20	٧
Gain-source voltage	V_{GSS}	±5	٧
Drain current	ID	3	Α
Power dissipation	P _D (Note 1)	20	W
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-45~150	°C

Note 1: Tc = 25°C (When mounted on a 1.6 mm glass epoxy PCB)

1. GATE 2. SOURCE (HEAT SINK) 3. DRAIN JEDEC JEITA TOSHIBA 2-5N1A

Marking



- 1. Gate
- 2. Source (heat sink)
- 3. Drain

Caution

Please take care to avoid generating static electricity when handling this transistor.



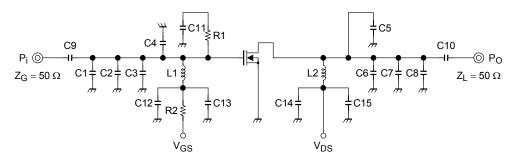
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain cut-off current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	5	μА
Gate-source leakage current	I _{GSS}	V _{GS} = 5 V	_	_	5	μА
Threshold voltage	V_{th}	$V_{DS} = 7.2 \text{ V}, I_D = 2 \text{ mA}$	0.55	1.05	1.55	V
Drain-source on-voltage	V _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 75 \text{ mA}$	_	18	_	mV
Forward transconductance	Y _{fs}	$V_{DS} = 7.2 \text{ V}, I_{DS} = 1 \text{ A}$	_	1	_	S
Input capacitance	C _{iss}	$V_{DS} = 7.2 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	53	_	pF
Output capacitance	C _{oss}	$V_{DS} = 7.2 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	49	_	pF
Output power	Po	V _{DS} = 7.2 V,	7	_	_	W
Drain efficiency	η _D	I _{idle} = 500 mA (V _{GS} = adjust),	60	_	_	%
Power gain	G _P	$f = 520 \text{ MHz}, P_i = 500 \text{ mW},$	11.4	_	_	dB
Low voltage output power	P _{OL}	$\begin{split} &V_{DS} = 6.0 \; V, \\ &I_{idle} = 500 \; mA \; (V_{GS} = adjust), \\ &f = 520 \; MHz, \; P_i = 500 \; mW, \end{split}$	5	_	_	W
Load mismatch	_	$\begin{split} &V_{DS} = 10 \text{ V, } P_O = 7 \text{ W,} \\ &V_{GS} = \text{adjust, } P_i = \text{adjust,} \\ &f = 520 \text{ MHz,} \\ &V\text{SWR LOAD 20:1 all phase} \end{split}$	No degradation			

Note 1: These characteristic values are measured using measurement tools specified by Toshiba.

Output Power Test Fixture

(Test Condition: f = 520 MHz, $V_{DS} = 7.2 \text{ V}$, $I_{idle} = 500 \text{ mA}$, $P_i = 500 \text{ mW}$)



C1: 15 pF C2: 11 pF L1: φ0.6 mm enamel wire, 5.8ID, 4T L2: φ0.6 mm enamel wire, 5.8ID, 8T R1: 2.2 Ω R2: 1.5 $k\Omega$

C3: 9 pF

C4: 30 pF

C5: 30 pF

C6: 11 pF

.o. 11 pi

C7: 8 pF C8: 9 pF

C9: 2200 pF

C10: 2200 pF

C11: 2200 pF

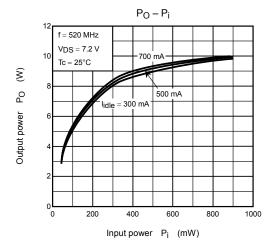
C12: 10000 pF

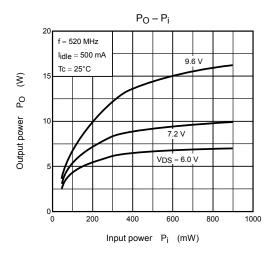
C13: 10 μF

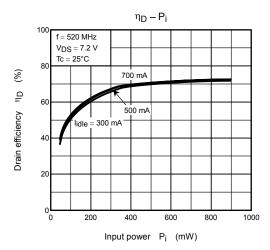
C14: 10000 pF

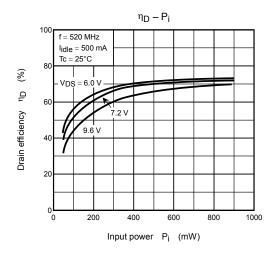
C15: 10 μF

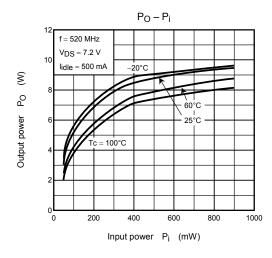
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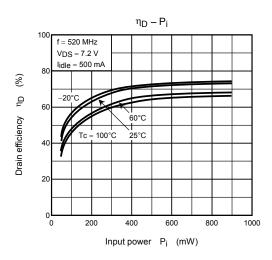












Note 2: These are only typical curves and devices are not necessarily guaranteed at these curves.

3 2004-12-14

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