

2SK4037

470 MHz Band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment

- Output power: $P_o = 35.50\text{dBmW}$ (3.5 W) (min)
- Gain: $G_p = 10.50\text{dB}$ (min)
- Drain Efficiency: $\eta_D = 60.0\%$ (typ)

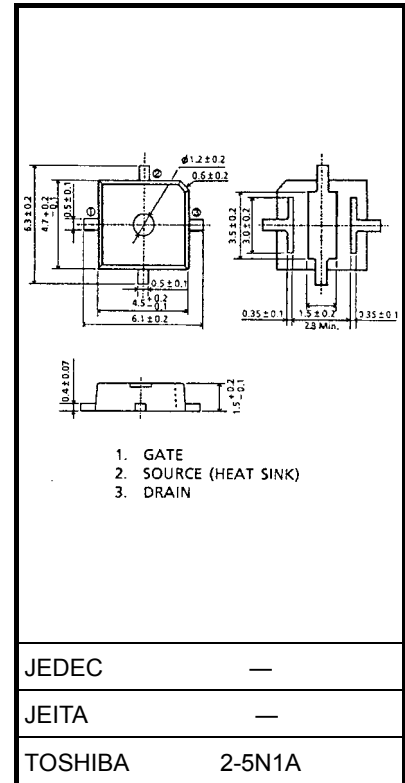
Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	12	V
Gate-source voltage	V_{GSS} (Note 1)	3	V
Drain current	I_D	3	A
Power dissipation	P_D (Note 2)	20	W
Channel temperature	T_{ch}	150	°C
Storage temperature range	T_{stg}	-45~150	°C

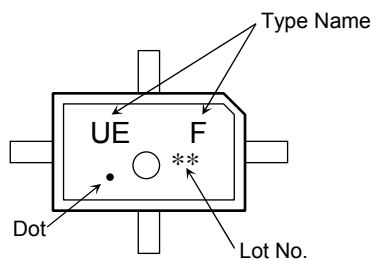
Note 1: Recommended Operation Condition: 0~3V

Note 2: $T_c = 25^\circ\text{C}$ (When mounted on a 0.8 mm glass epoxy PCB)

Unit: mm



Marking



Caution: This device is sensitive to electrostatic discharge.

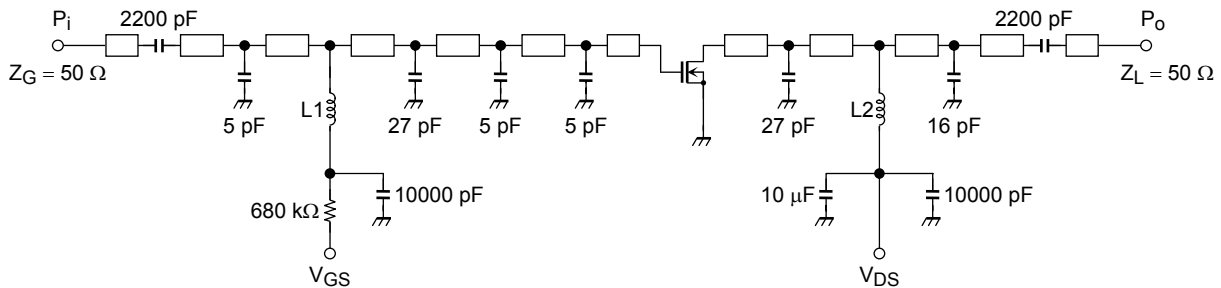
Please make enough tool and equipment earthed when you handle.

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output power	P_O	$V_{DS} = 6.0 \text{ V}$, $I_{D} = 250 \text{ mA}$ ($V_{GS} = \text{adjust}$) $f = 470 \text{ MHz}$, $P_i = 25 \text{ dBmW}$ $Z_G = Z_L = 50 \Omega$	35.5	36.0	—	dBmW
Drain efficiency	η_D		55.0	60.0	—	%
Power gain	G_p		—	11.0	—	dB
Threshold voltage	V_{th}	$V_{DS} = 6.0 \text{ V}$, $I_D = 0.5 \text{ mA}$	—	1.0	1.5	V
Drain cut-off current	I_{DSS}	$V_{DS} = 12 \text{ V}$, $V_{GS} = 0 \text{ V}$	—	—	10	μA
Gate-source leakage current	I_{GSS}	$V_{GS} = 3 \text{ V}$, $V_{DS} = 0 \text{ V}$	—	—	5	μA
Load mismatch (Note 2)	—	$V_{DS} = 6.0 \text{ V}$, $f = 470 \text{ MHz}$, $P_i = 25 \text{ dBmW}$, $P_o = 35.5 \text{ dBmW}$ ($V_{GS} = \text{adjust}$) VSWR LOAD 10:1 all phase	No degradation			—

Note 2: These characteristic values are measured using measurement tools specified by Toshiba.

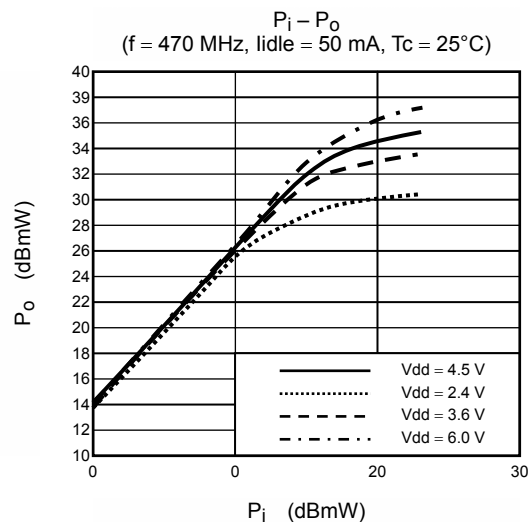
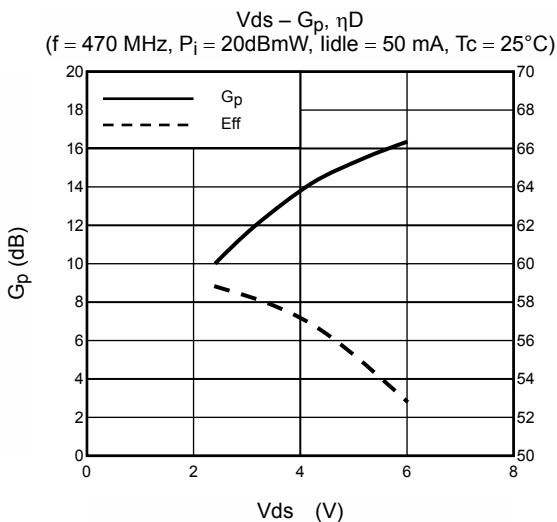
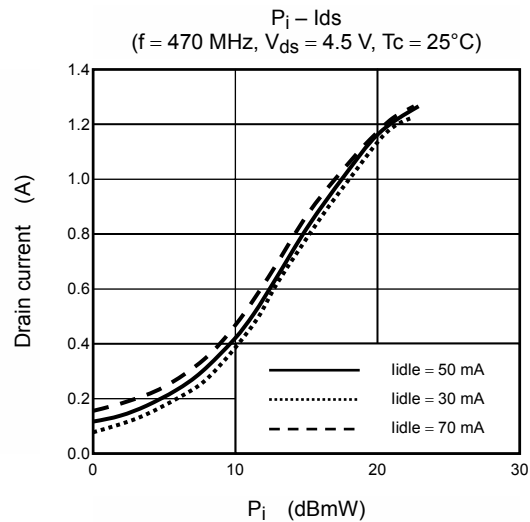
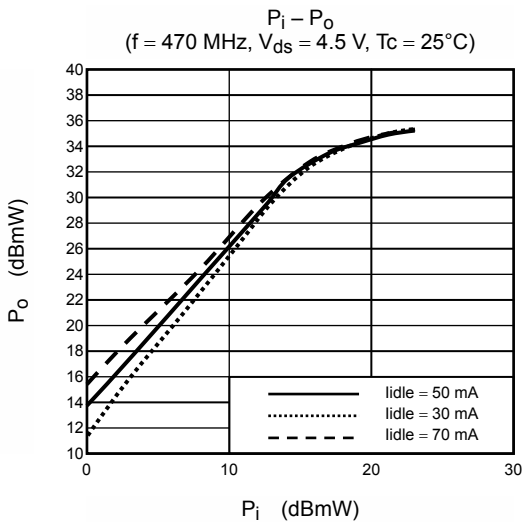
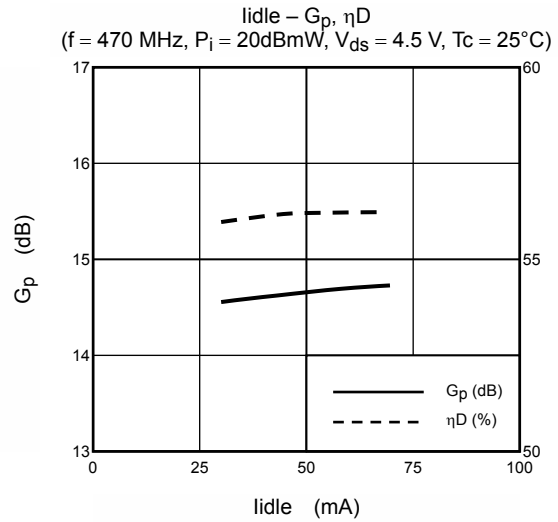
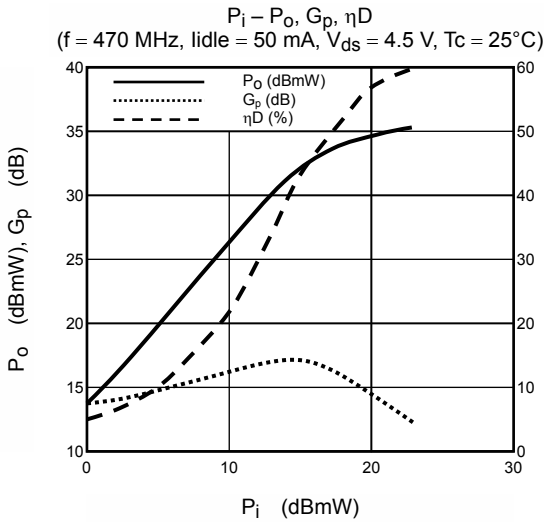
Test Circuit

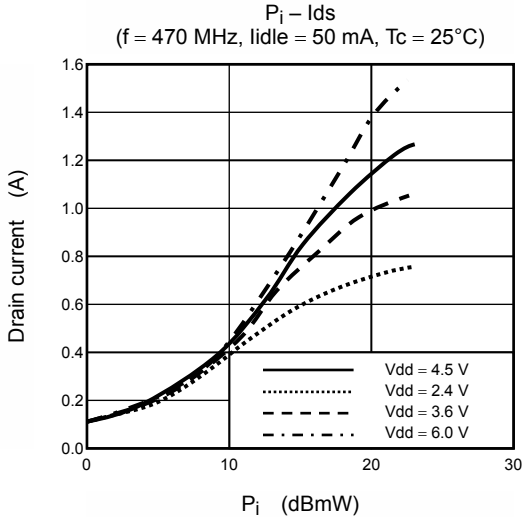


L1: $\phi 0.6 \text{ mm}$ enamel wire, 5.8ID, 8T

L2: $\phi 0.6 \text{ mm}$ enamel wire, 5.8ID, 8T

Line: 2mm





Caution: These are typical curves and devices are not necessarily guaranteed at these curves.

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