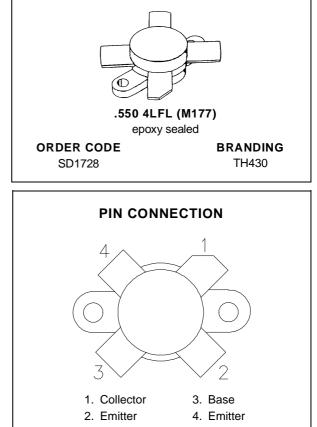


## RF & MICROWAVE TRANSISTORS HF SSB APPLICATIONS

- OPTIMIZED FOR SSB
- 30 MHz
- 50 VOLTS
- ∎ IMD 30 dB
- GOLD METALLIZATION
- COMMON EMITTER
- POUT = 250 W PEP WITH 14.5 dB GAIN



## DESCRIPTION

The SD1728 is a 50 V epitaxial silicon NPN planar transistor designed primarily for SSB and VHF communications. This device utilizes emitter ballasting for improved ruggedness and reliability.

ABSOLUTE MAXIMUM RATINGS	$(T_{case} = 25^{\circ}C)$
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Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage	110	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	55	V	
V <sub>EBO</sub>	Emitter-Base Voltage	4.0	V	
lc	Device Current	40	А	
PDISS	Power Dissipation	330	W	
TJ	Junction Temperature	+200	°C	
T <sub>STG</sub>	Storage Temperature	– 65 to +150	°C	

#### THERMAL DATA

R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance	0.4	°C/W
November 1992			1/9

#### **ELECTRICAL SPECIFICATIONS** $(T_{case} = 25^{\circ}C)$

#### STATIC

Symbol	Test Conditions	Value			Unit		
			Min.	Тур.	Max.	om	
BVCES	$I_C = 200 \text{mA}$	$V_{BE} = 0V$		110			V
BVCEO	$I_C = 200 \text{mA}$	$I_B = 0mA$		55	_		V
BVEBO	$I_E = 20 \text{mA}$	$I_C = 0 m A$		4.0	_	_	V
ICEO	$V_{CE} = 30V$	$I_E = 0mA$			_	10	mA
ICES	$V_{CE} = 60V$	$I_E = 0mA$		—	_	10	mA
hFE	$V_{CE} = 6V$	Ic = 10A		15		45	—

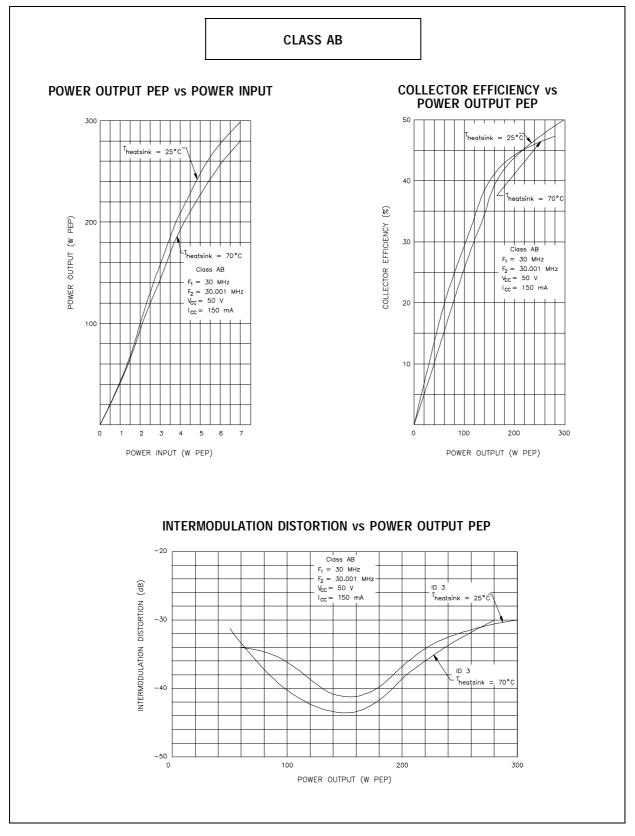
#### DYNAMIC

Symbol	Test Conditions			Value			Unit
Symbol				Min.	Тур.	Max.	Unit
Роит	f = 30 MHz	$V_{CC} = 50 V$	$I_{CQ} = 150 \text{ mA}$	250	—		W
G <sub>P</sub> *	Pout = 250 W PEP	$V_{CC} = 50 V$	$I_{CQ} = 150 \text{ mA}$	14.5	_	—	dB
IMD*	$P_{OUT} = 250 \text{ W PEP}$	$V_{CC} = 50 V$	$I_{CQ} = 150 \text{ mA}$	_	—	-30	dBc
ηc*	$P_{OUT} = 250 \text{ W PEP}$	$V_{CC} = 50 V$	$I_{CQ} = 150 \text{ mA}$	37	_		%
C <sub>OB</sub>	f = 1 MHz	$V_{CB} = 50 V$		_		360	pF

Note: \* Two Tone Method;  $f_1 = 30.00 \text{ MHz}$ ;  $f_2 = 30.001 \text{ MHz}$ In Class C:  $G_P$  Min. 13.5 dB, Efficiency 65%@ 30MHz  $G_P$  Min. 10 dB, Efficiency 57%@ 70MHz

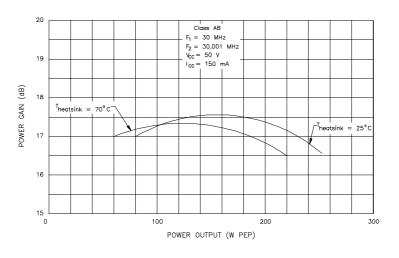


#### TYPICAL PERFORMANCE



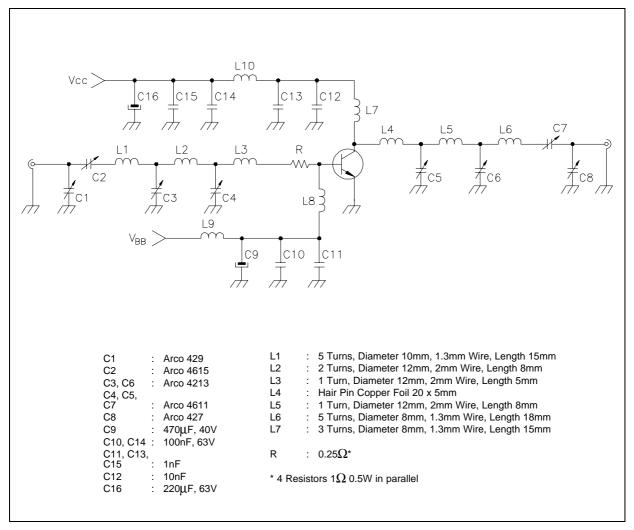


#### **TYPICAL PERFORMANCE (cont'd)**

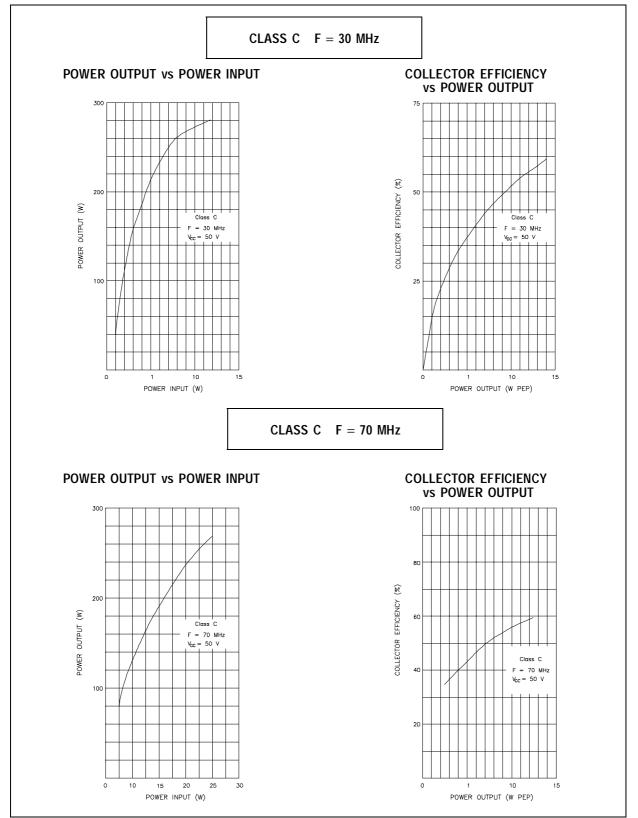


#### POWER GAIN vs POWER OUTPUT PEP

#### **TEST CIRCUIT SSB - CLASS AB - 30 MHz**

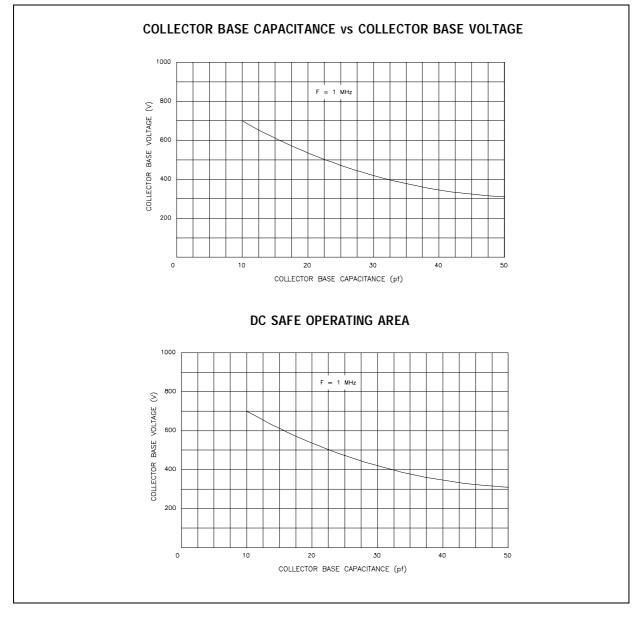


#### **TYPICAL PERFORMANCE**



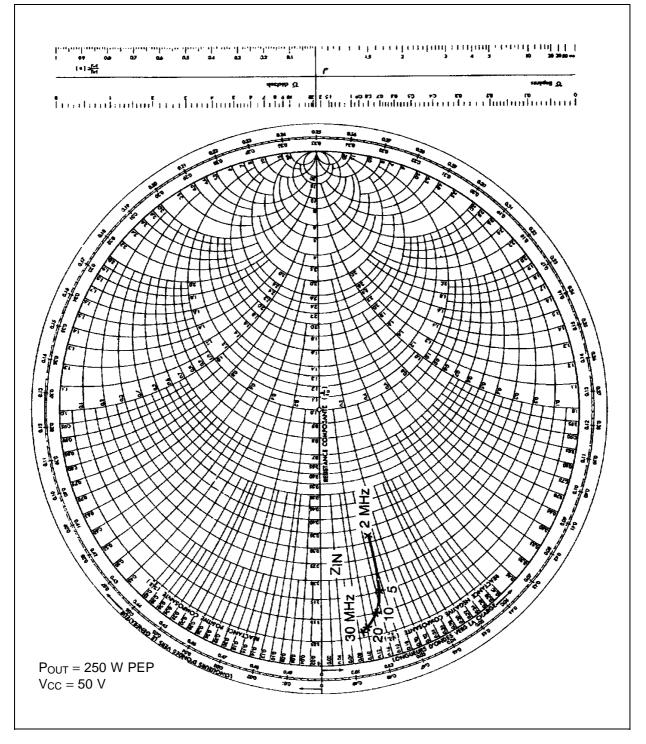


### **TYPICAL PERFORMANCE (cont'd)**



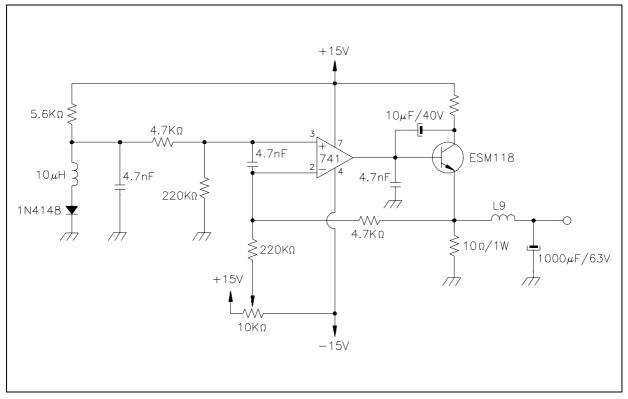


#### IMPEDANCE DATA (TYPICAL)

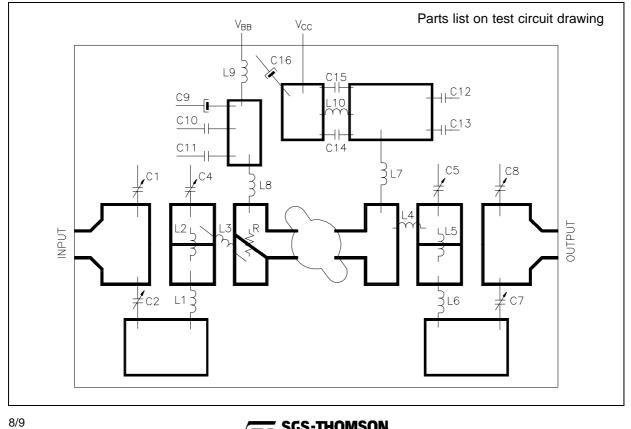




## **BIAS CIRCUIT**

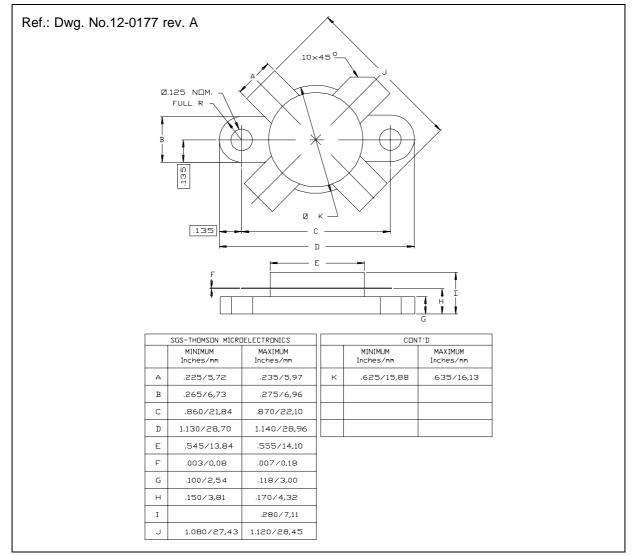


#### **MOUNTING CIRCUIT**



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#### PACKAGE MECHANICAL DATA



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